

FIG. 1

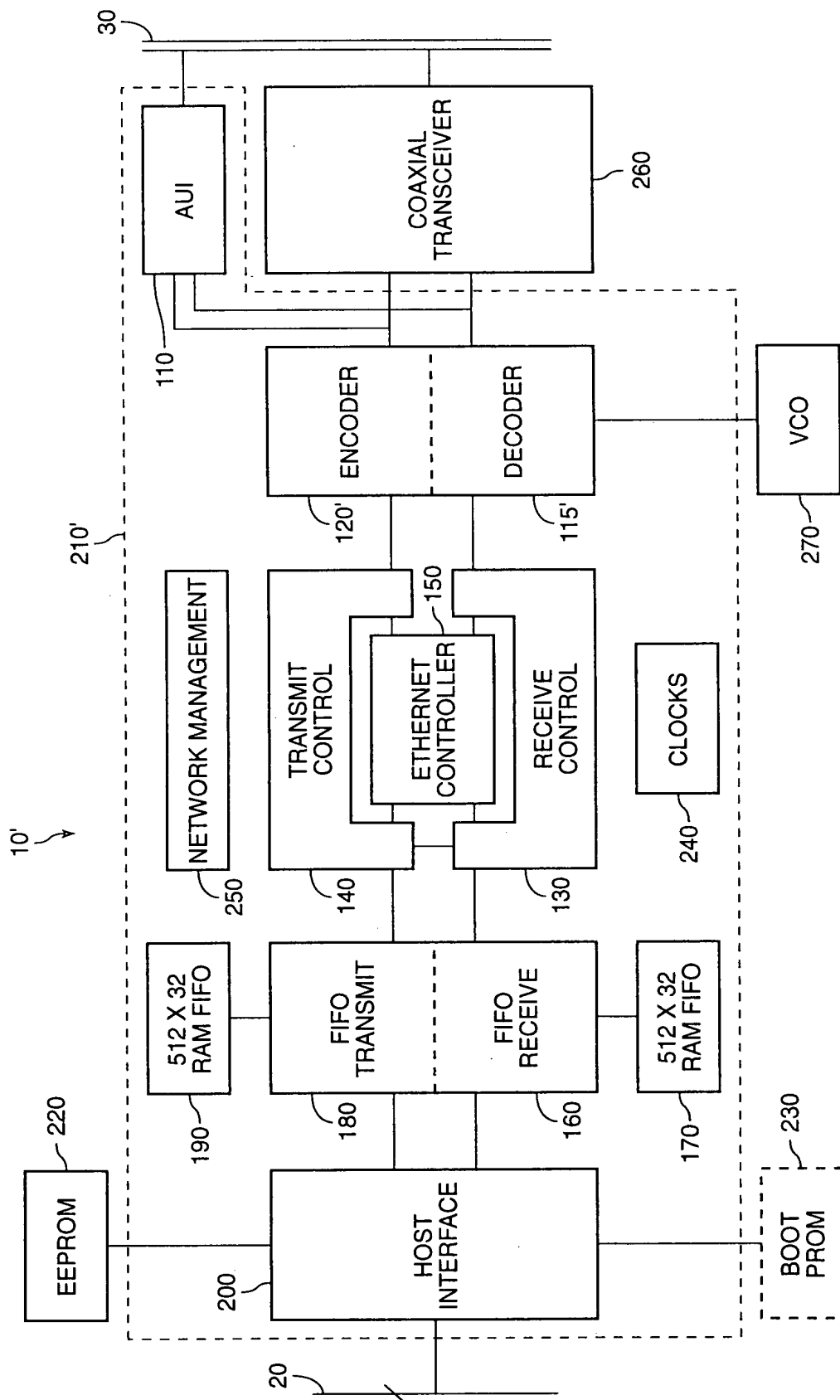


FIG. 2

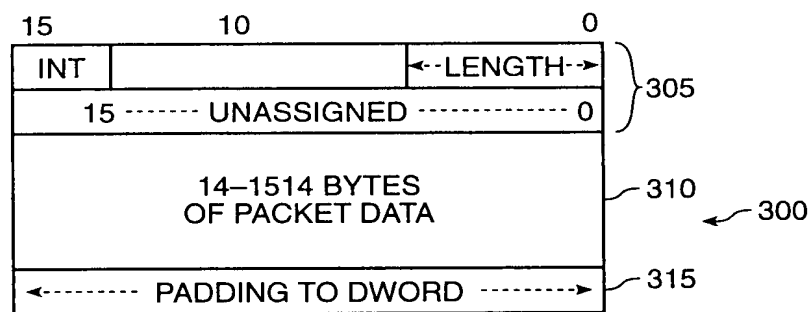
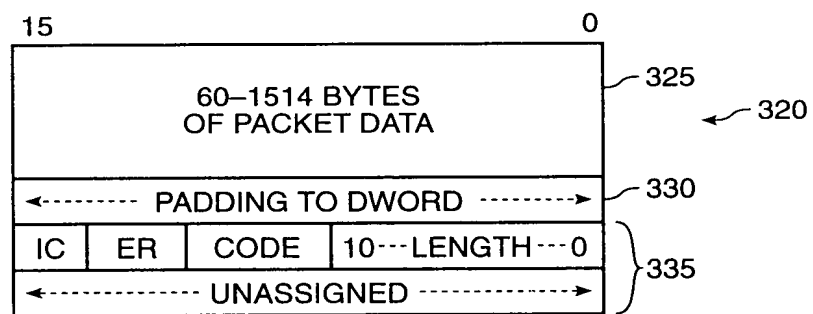


FIG. 3A



**FIG. 3B**

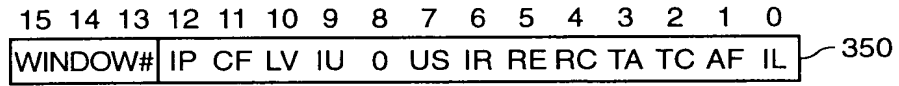


FIG. 3C

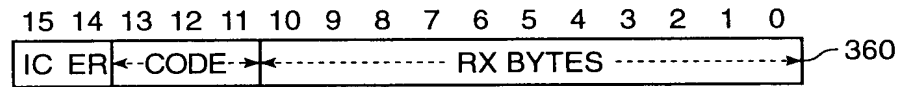


FIG. 3D

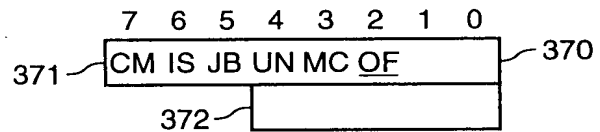


FIG. 3E

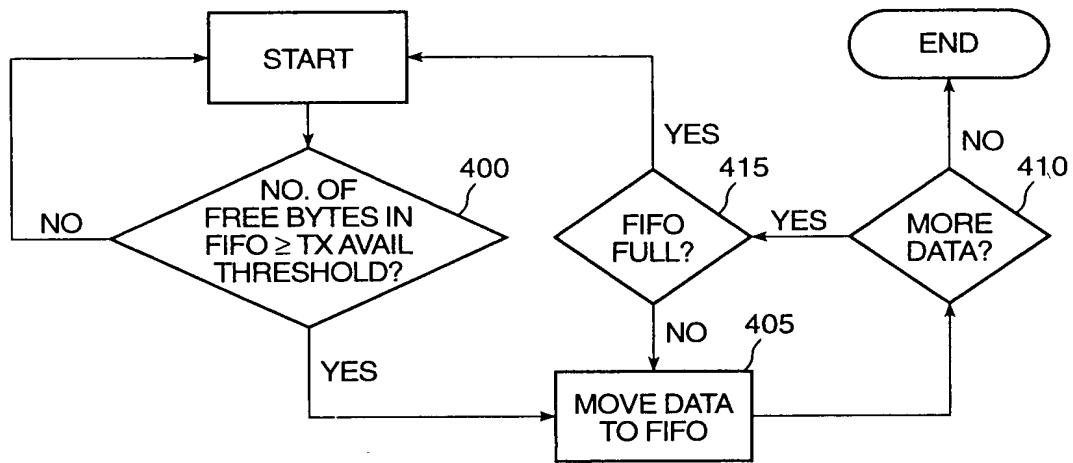


FIG. 4A

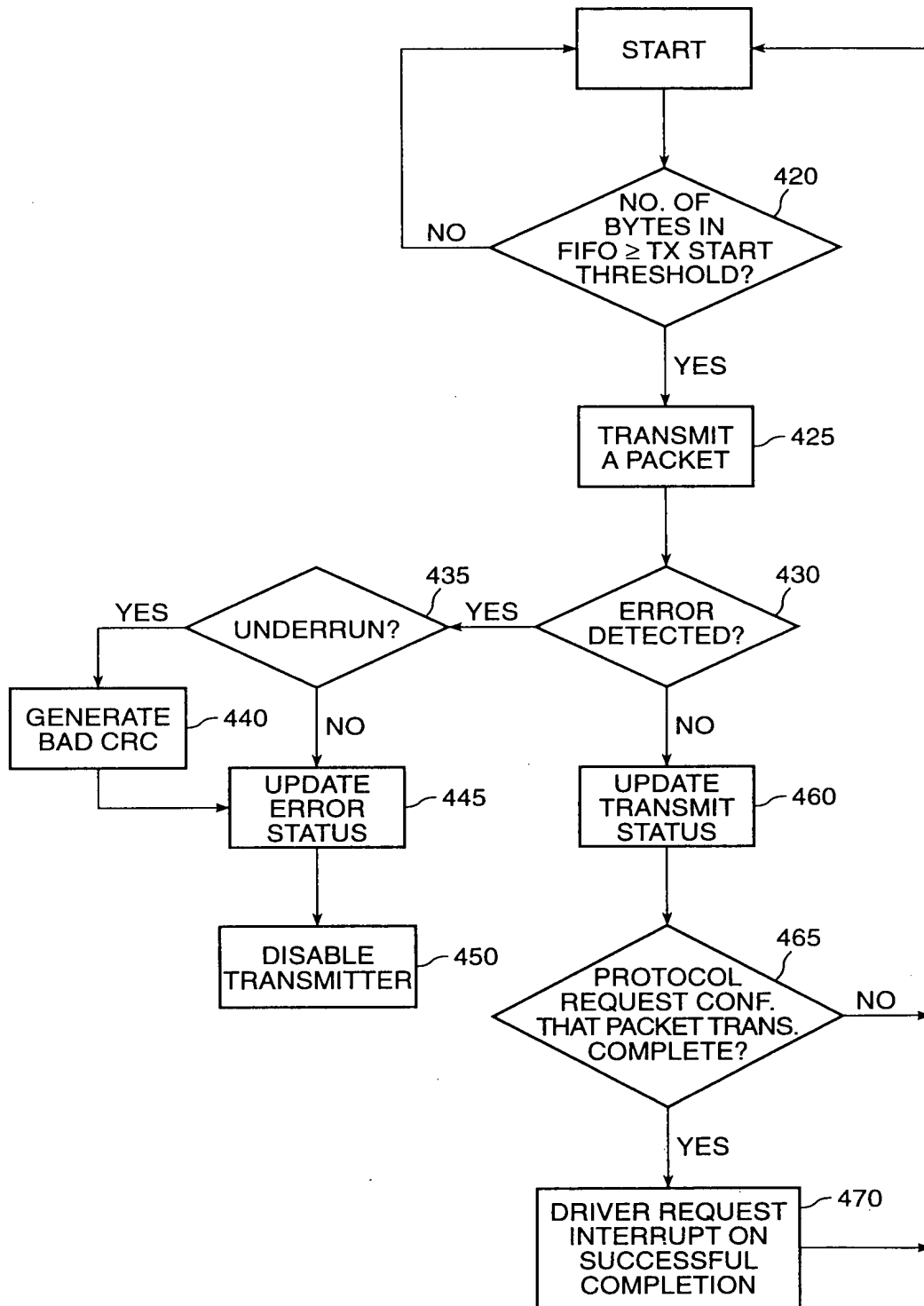


FIG. 4B

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graph TD
    500[WAIT FOR NEW PACKET] --> 505[RECEIVE PREAMBLE]
    505 --> 510[RECEIVE AND STORE BYTE]
    510 --> 515[INCREMENT RX BYTES]
    515 --> 520{RX FREE BYTES = DMA THRESHOLD?}
    520 -- YES --> 525[DMA]
    520 -- NO --> 530{ENTIRE PACKET RECEIVED?}
    530 -- YES --> 535[ADJUST RX STATUS]
    535 --> 540[RX COMPLETE INTERRUPT]
    540 --> 500
    530 -- NO --> 545{EARLY RX INTERRUPT ENABLED?}
    545 -- NO --> Exit1[ ]
    545 -- YES --> 550{RX BYTES ≥ EARLY RX THRESHOLD?}
    550 -- NO --> Exit2[ ]
    550 -- YES --> 555[EARLY RX INTERRUPT]
    555 --> Exit3[ ]

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**FIG. 5**

ACKNOWLEDGE  
EARLY RX  
INTERRUPT 600

